

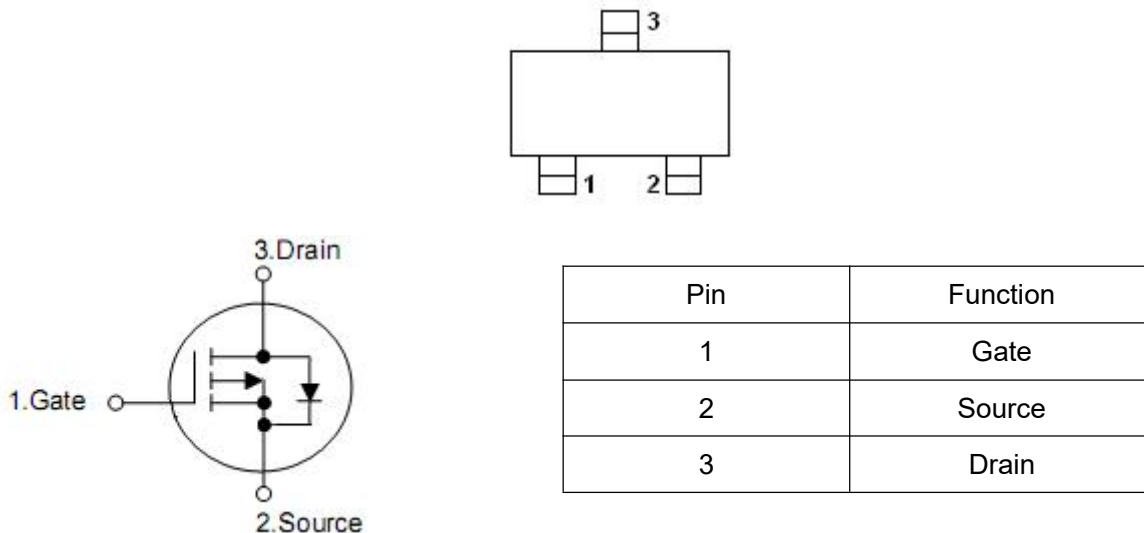
1. Description

The KIA3401 uses advanced trench technology to provide excellent $R_{DS(on)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications. Standard Product KIA3401 is Pb-free (meets ROHS & Sony 259 specifications). KIA3401 is a Green Product ordering option.

2. Features

- $V_{DS}(V) = -30V$
- $I_D = -4.0A$
- $R_{DS(on)} < 60m\Omega (V_{GS} = -10V, I_D = -4.0A)$
- $R_{DS(on)} < 70m\Omega (V_{GS} = -4.5V, I_D = -3.5A)$
- $R_{DS(on)} < 100m\Omega (V_{GS} = -2.5V, I_D = -1.2A)$

3. Symbol



4. Absolute maximum ratings

(T_A=25°C, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-source voltage	V _{DS}	-30	V
Gate-source voltage	V _{GS}	±12	V
Continuous drain current ^A T _A =25°C	I _D	-4.0	A
Pulsed drain current ^B	I _{DM}	-25	A
Total power dissipation ^A T _A =25 °C	P _D	1.2	W
Junction and storage temperature range	T _J , T _{STG}	-55 to 150	°C

5. Thermal characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance from Junction to Ambient ^A (t≤5s)	R _{θJA}	-	104	°C/W

6. Electrical characteristics

($T_A=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain-source breakdown voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-30	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=-24V, V_{GS}=0V$	-	-	-1	μA
Gate- body leakage current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.6	-1	-1.3	V
On state drain current	$I_{D(on)}$	$V_{GS}=-4.5V, V_{DS}=-5V$	-25	-	-	A
Static drain-source on-resistance(note a)	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-4.0A$	-	55	60	m Ω
		$V_{GS}=-4.5V, I_D=-3.5A$	-	65	70	
		$V_{GS}=-2.5V, I_D=-1.2A$	-	95	100	
Forward transconductance(note a)	g_{fs}	$V_{DS}=-5.0V, I_D=-4.0A$	7	10	-	S
Diode forward voltage	V_{SD}	$V_{GS}=0V, I_S=-1A$	-	-	-1.2	V
Dynamic characteristics (note b)						
Input capacitance	C_{iss}	$V_{DS}=-15V, V_{GS}=0V,$ $f=1\text{MHz}$	-	951	-	pF
Output capacitance	C_{oss}		-	115	-	
Reverse transfer capacitance	C_{rss}		-	77	-	
Gate resistance	R_g	$V_{DS}=0V,$ $V_{GS}=0V, f=1\text{MHz}$	-	6	-	Ω
Total gate charge	Q_g	$V_{DS}=-15V, V_{GS}=-4.5V$ $I_D=-4.0A$	-	9.0	-	nC
Gate-source charge	Q_{gs}		-	1.6	-	
Gate-drain charge	Q_{gd}		-	3.0	-	
Switching Characteristics (note b)						
Turn-on delay time	$t_{d(on)}$	$V_{DS}=-15V, I_D=-4A,$ $R_{GEN}=6\Omega, V_{GS}=-10V$	-	7.0	-	ns
Rise time	t_r		-	3.0	-	
Turn-off delay time	$t_{d(off)}$		-	30	-	
Fall time	t_f		-	12	-	
Reverse recovery time	t_{rr}	$I_F=-4A, di/dt=100A/\mu s,$	-	20.2	-	nS
Reverse recovery charge	Q_{rr}		-	11.2	-	nC

Note:

- Pulse Test : Pulse Width < 300 μs , Duty Cycle $\leq 2\%$.
- These parameters have no way to verify.

7. Test circuits and waveforms

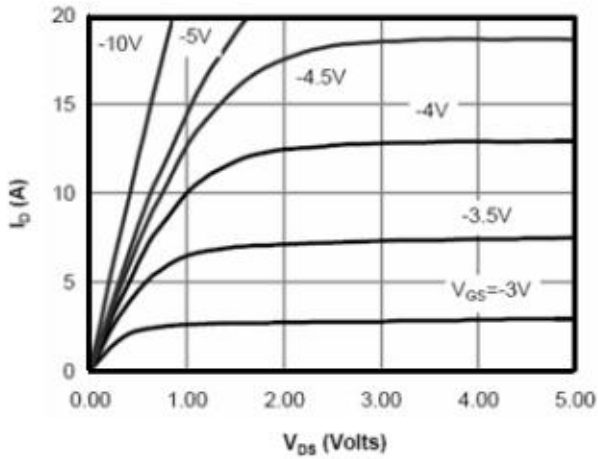


Fig 1: On-Region Characteristics

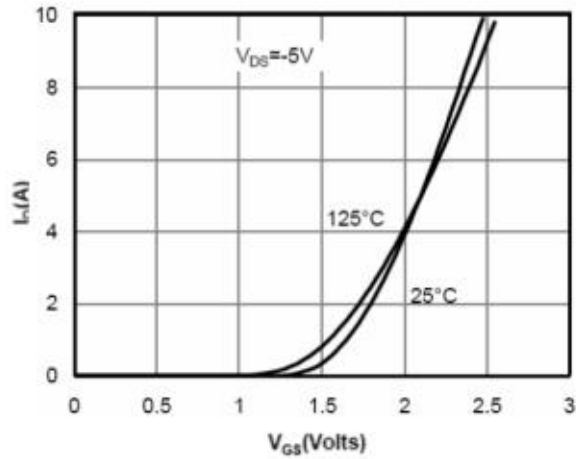


Figure 2: Transfer Characteristics

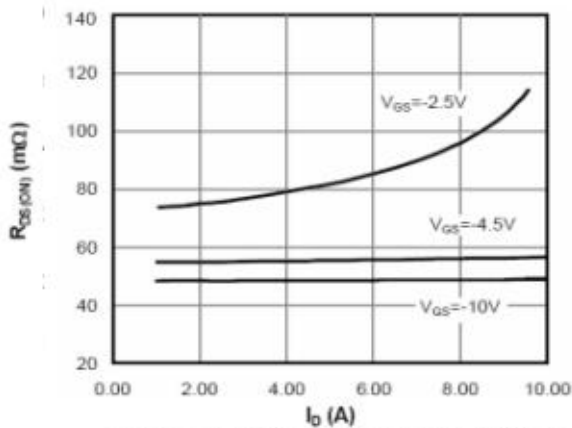


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

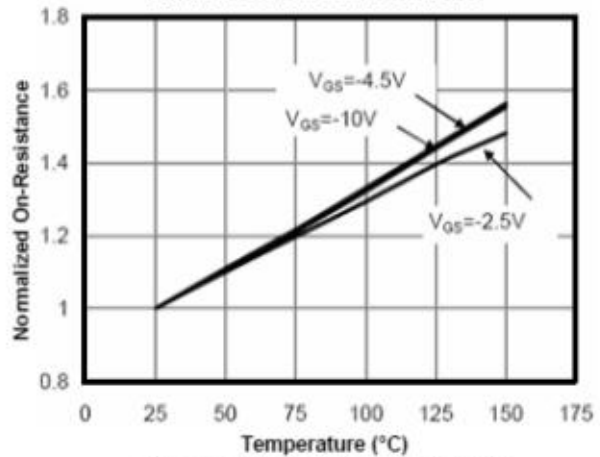


Figure 4: On-Resistance vs. Junction Temperature

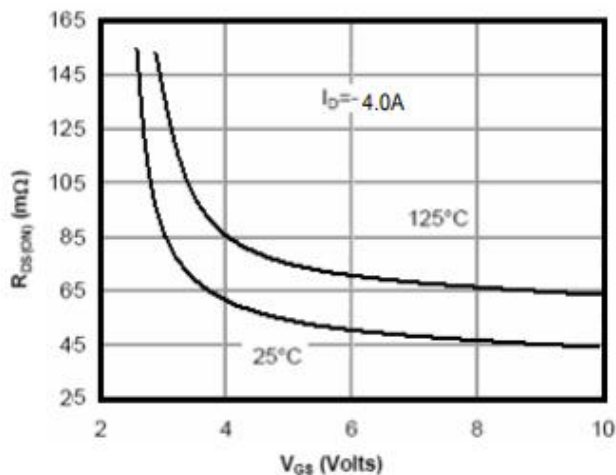


Figure 5: On-Resistance vs. Gate-Source Voltage

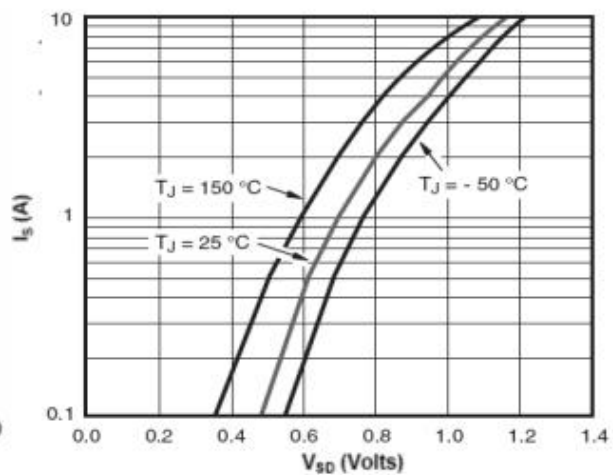


Figure 6: Body-Diode Characteristics

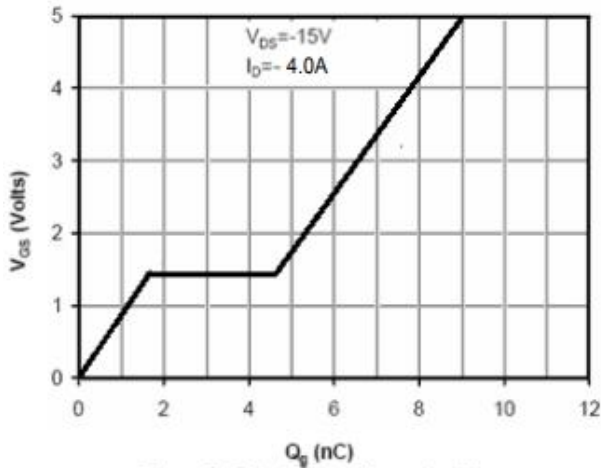


Figure 7: Gate-Charge Characteristics

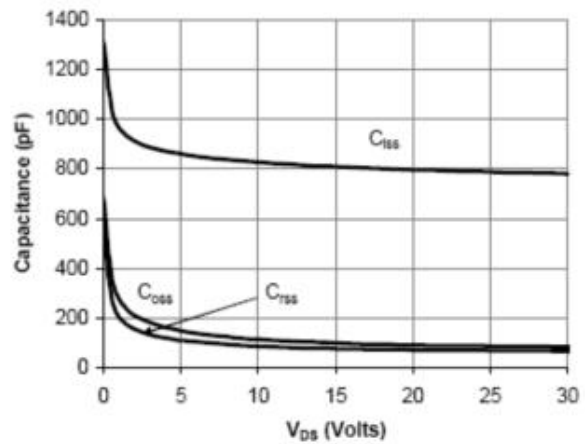


Figure 8: Capacitance Characteristics

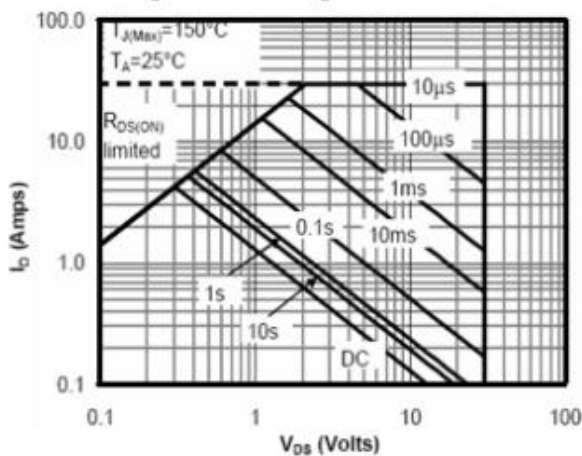


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

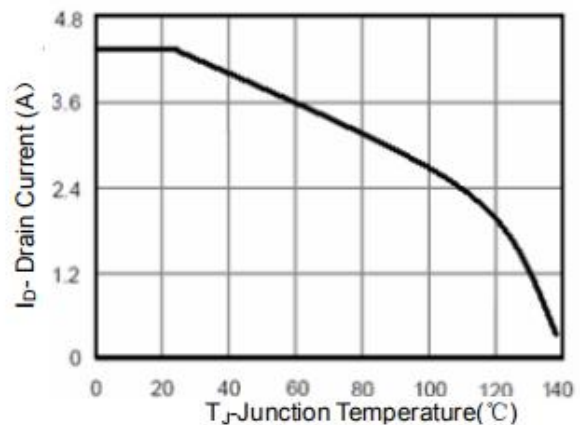


Figure 10: Drain Current

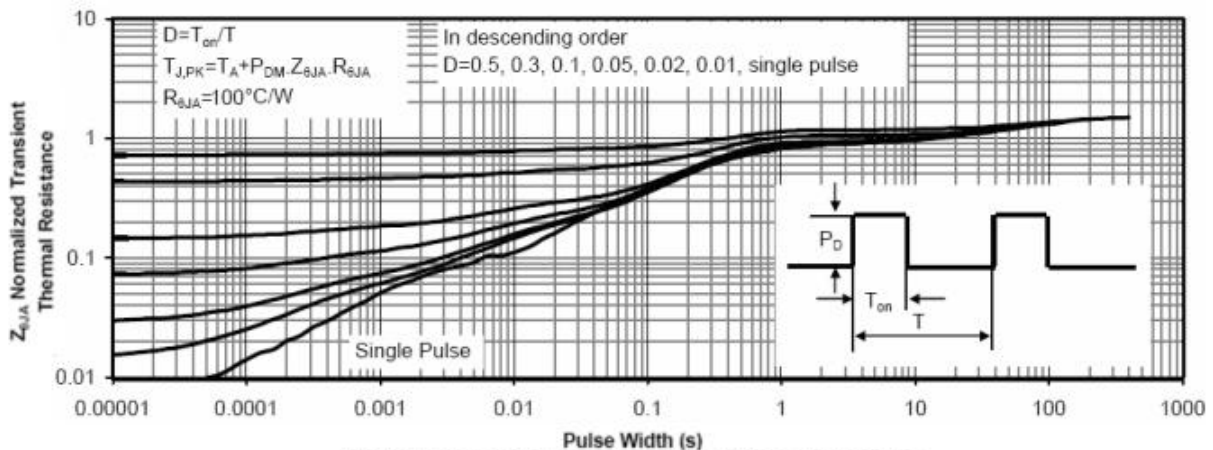


Figure 11: Normalized Maximum Transient Thermal Impedance